

Company Press Release



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Tallika Selects Avery Design Systems to develop PCI Express Design IP Solution

ANDOVER, MA., June 8, 2004 – Avery Design Systems Inc., the leader in verification solutions for PCI Express*, today announced that Tallika, Corp. has licensed Avery’s PCI Express verification tools for the development of its next-generation semiconductor IP and chips targeting applications in networking, computing, storage, and consumer electronics.

Tallika engineers are using the PCI-Xactor PCI Express verification IP to simulate and verify the PCI Express interface of the next generation Tallika semiconductor IP and chips. The use of PCI-Xactor helps the Tallika team eliminate bugs at the pre-silicon stage at a much faster rate than they had anticipated, reducing time-to-market while having higher quality end products.

“Avery’s verification framework has enabled us to bring up our endpoint testbench in a short time and begin exercising our design thoroughly using their compliance, error, and stress tests,” said Hemi Bhatnagar, President, CEO of Tallika. “Avery’s open source Verilog models and testcases provide us with the level of control we need over the verification environment to sensitize all corner cases of our design.”

“Avery is excited to partner with Tallika’s world class design team in the development and delivery of PCI Express IP solutions,” said Chilai Huang, President of Avery Design Systems. “Tallika’s team has a long history of delivering first time production silicon for multi-million gate chips – their choice of Avery’s solution is an endorsement of the quality of our solution. I am confident that this partnership of Tallika’s design expertise and Avery’s Verification expertise will result in an industry leading comprehensive PCI Express solution.”

About PCI-Xactor for PCI Express

The PCI-Xactor for PCI Express Verification Solution is a complete verification solution consisting of Bus Function Model (BFM), SuperMonitor, and test suites

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and verification frameworks for functional verification of PCI Express components. The PCI-Xactor allows design and verification engineers to quickly and extensively test the entire functionality of their PCI Express compliant devices. Verification frameworks form complete testbench environments for endpoint, switch, and bridge designs. Verification engineers just need to replace an Avery BFM with their design and begin running comprehensive verification tests. The PCI-Xactor environment leverages advanced verification techniques of Avery's TestWizard product supporting complex data structures, transaction database, random generation, temporal property checking, and coverage analysis.

Key Features

- ? Complete set of fully functional BFMs and testbenches for every PCI Express component: Endpoints, Root Complex, Switch, and PCI/PCI-X to PCI Express Bridge
- ? Root Complex model provides the enumeration functions which allows the designer to verify designs earlier to catch driver setup problems
- ? Support for serial, 10b-bit symbol, and PIPE interfaces.
- ? Robust BFM API automates sending TLP/DLLP and controlling BFM device response and link state
- ? Inject errors and noise at various layers
- ? Test suites include compliance tests from PCI-SIG, random, error, and link-level stress tests
- ? Test are developed as self-checking, portable, and reusable on any type of design
- ? SuperMonitor verifies transaction ordering in N-port switch and bridge designs
- ? Native programming interfaces for Specman, Vera, C/C++

About PCI Express technology

PCI Express technology is the new industry-standard I/O targeted to provide local connectivity across desktop, mobile, enterprise and communications platforms. PCI Express resides at the center of enterprise interconnect innovations anticipated across storage, networking, and clustering and workstations. Next-generation servers, utilizing PCI Express technology, will offer powerful and cost-effective computing platforms, scalable hardware building blocks, market-tested best-of-breed solutions, and enterprise-class reliability, availability, serviceability and manageability.

About Avery Design Systems

Avery Design Systems Inc. is a supplier of functional verification products and service that enables dramatic productivity improvements of the ASIC-based

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systems and SOC verification process. Additional information about Avery Design Systems is available at <http://www.avery-design.com>.

About Tallika Corporation

Tallika Corporation is a leading provider of Silicon Intellectual Property and Professional Services for consumer, networking, computing, and storage markets. Tallika's core team has taken several projects from Concept-To-Production including multiple 50 Million+ transistor ASICs with first time production ready silicon. Tallika's team is comprised of industry veterans from Companies such as Philips Semiconductor/VLSI Technology, Corrent, Intel, Conexant, Agere, Ross Technologies and Synopsys. Tallika's core expertise includes multi-gigabit Internet Security (Firewall/VPN/Intrusion Detection), ARM/MIPS/ARC based multi-processor SOC's, Industry standard busses (PCI Express/PCI-X, SPI 3, Utopia 3, SPI 4, AHB, PLB, etc), analog/mixed signal designs, and memory interfaces.

For more information on Tallika's Products and Services, please visit <http://www.tallika.com>

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