



“We engaged with Tallika for execution of our product development plans. The engagement included Market Requirements Document to GDSII ASIC implementation, FPGA Prototype of the ASIC, and firmware development. The design challenges included extremely low power mixed signal ASIC design, selection and integration of IP from several sources into the ASIC, and system level design and integration.

Tallika’s responsibility included creation of Product Requirements Document, Micro-Architecture Spec for the SOC, as well as execution of the project using its India Design Center. Validity also licensed Tallika’s Secure SOC Platform IP including - AES256, SHA256/SHA1, AHB sub-system, DMA Controller and 2048-bit native exponentiation/RSA acceleration engine.

Tallika also delivered a Xilinx Virtex4 based FPGA prototype for our SOC using its Secure SOC Platform. Tallika’s deliverables included integration of Tallika’s security API library and RSA acceleration library and firmware development.

I am very impressed and pleased by the results delivered by Tallika. As a startup, Validity cannot afford to risk failure on its Product Development - Tallika has been a reliable design services and IP partner for Validity, delivering quality work from its India Design Center under tremendous pressure and aggressive deadlines.

We have a first pass silicon success and the FPGA Secure SOC platform is being used across our engineering teams for accelerating product development. Validity has decided to expand its relationship to develop next generation solutions using Tallika’s Design Services.”

Alex Erhart
Executive Vice President, Products
Validity Sensors Inc